| Ref<br># | Hits | Search Query  | DBs   | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|---|---|---------------------|---------|------------------|
| L1       | 1    | 361/695.ccls. and (inter near integrated near circuit)    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/06/01 15:57 |
| L2       | 249  | 361/695.ccls. and (integrated near circuit)               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/06/01 15:59 |
| L3       | 14   | 361/695.ccls. and vga                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/06/01 16:01 |
| L4       | 5    | 361/719.ccls. and vga                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/06/01 16:01 |
| S1       | 0    | 702/179.ccls. and (inter near integrated near circuit)    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/06/01 15:56 |
| S2       | 57   | 702/179.ccls. and (integrated near circuit)               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/02/23 14:45 |
| 53       | 521  | (component near monitoring) and (integrated near circuit) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/02/23 14:46 |
| S4       | 148  | S3 and bus and display                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2005/02/23 15:05 |

| S5  | 0     | S3 and (vga near card)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/24 15:56 |
|-----|-------|--|---|------|----|------------------|
| S6  | 176   | S3 and card  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR . | ON | 2005/02/23 16:28 |
| S7  | 32765 | (testing or monitoring) and component and circuit and card                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/23 16:29 |
| S8  | 1035  | S7 and vga   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/23 16:30 |
| S9  | 189   | S8 and temperature and fan   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/23 16:33 |
| S10 | 103   | (integrated near circuit) and (vga<br>near card)                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/24 10:56 |
| S11 |       | (integrated near circuit) and (vga<br>near card) and bus and fan               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/24 10:57 |
| S12 | 0     | (inter near integrated near circuit)<br>and (vga near card) and bus and<br>fan | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/24 10:58 |

| S13 | 0   | (inter near integrated near circuit) and (vga near card) and bus | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 10:58 |
|-----|-----|--|---|----|----|------------------|
| S14 | 4   | (inter near integrated near circuit) and (vga near card)         | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 10:59 |
| S15 | 39  | 345/519.ccls. and vga  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/03 16:09 |
| S16 | 53  | 345/520.ccls. and vga  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 11:18 |
| S17 | 405 | vga and temperature and bus and<br>(integrated adj circuit)      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 11:19 |
| S18 | 110 | S17 and fan  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 12:03 |
| S19 | 276 | (vga near card) and bus and display                              | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 13:02 |
| S20 | 43  | (vga near card) and bus and display and temperature              | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/02/24 13:17 |

| S21 | 16  | (vga near card) and cool and fan   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/02/24 13:18 |
|-----|-----|--|---|------|----|------------------|
| S22 | 29  | (vga with card) and temperature -and thermal and cooling                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/03/11 16:48 |
| S23 | 14  | vga and (inter near integrated<br>near circuit) and display and<br>processor and fan | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/03 16:12 |
| S24 | 87  | (inter near integrated near circuit) and bus and display and processor and fan       | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/04 13:53 |
| S25 | 2   | "4903222".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/04 14:02 |
| S26 | 154 | detection and (inter adj integrated adj circuit) and display and processor and bus   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/04 14:52 |
| S27 | 21  | computer near component near monitoring  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR . | ON | 2005/05/04 16:33 |
| S28 | 2   | "4683568".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/04 16:12 |

| S29 | 2  | "5367670".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/04 16:41 |
|-----|----|--|---|----|----|------------------|
| S30 | 2  | "5225997".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:00 |
| S31 | 0  | detector and regulator and<br>(temperature and frequency with<br>GPU) and (operation adj status) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:01 |
| S32 | 0  | detector and regulator and<br>(temperature and frequency with<br>GPU)                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:01 |
| S33 | 0  | (detector or regulator) and<br>(temperature and frequency with<br>GPU)                           | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:01 |
| S34 | 17 | (detector or regulator) and (temperature and frequency and GPU)                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:05 |
| S35 | 0  | regulator and temperature and frequency and GPU and bus and (inter adj integrated adj circuit)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:06 |
| S36 | 5  | regulator and temperature and frequency and GPU and bus  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/05 09:20 |

| S37 | 0  | (GPU near temperature near monitoring) and bus and status | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;            | OR . | ON | 2005/05/05 09:21 |
|-----|----|---|---|------|----|------------------|
| S38 | 0  | (GPU near temperature near monitoring) and bus            | IBM_TDB US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB        | OR   | ON | 2005/05/05 09:21 |
| S39 |    | (GPU near temperature near monitoring)                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/05 10:20 |
| S40 | 2  | "5485625".pn.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/05 11:10 |
| S41 | 7  | (integrated near circuit near<br>thermal near management) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/05 13:23 |
| S42 | 2  | "5948106".pn.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/05 14:20 |
| S43 | 2  | (vga near card) and (thermal near management)             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/05 16:17 |
| 544 | 20 | (vga near card) and cool                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/05 16:59 |

| C45 |     | ,  | LIC DCC: IS   |    | ON. | 2005/05/05 17 05 |
|-----|-----|--|---|----|-----|------------------|
| S45 | 541 | (vga near card)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/05 17:00 |
| S46 | 19  | S45 and regulate   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/05 17:01 |
| S47 | 33  | S45 and temperature and frequency  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/05 17:02 |
| S48 | 1   | ((temperature and frequency) near GPU) and monitor   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/09 14:16 |
| S49 | 1   | ((temperature and frequency) near GPU)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/09 14:16 |
| S50 | 9   | temperature and frequency and GPU and card   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/09 16:12 |
| S51 | 2   | "5767896".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:07 |
| S52 | 37  | detection and (inter adj integrated<br>adj circuit) and display and<br>processor and regulator | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:19 |

|     |   | •  |   |    |     |                  |
|-----|---|--|---|----|-----|------------------|
| S53 | 0 | (vga near controller) and (thermal<br>near sensor) and regulator | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:20 |
| S54 | 0 | (vga near controller) and (thermal near sensor)                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:20 |
| S55 | 3 | (vga with controller) and (thermal near sensor)                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:20 |
| S56 | 0 | vga and fan and GPU and detector<br>and bus and display          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:21 |
| S57 | 0 | vga and fan and GPU and detector<br>and bus                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:21 |
| S58 | 0 | vga and fan and GPU and detector                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:21 |
| S59 | 5 | vga and fan and GPU  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/05/10 10:39 |
| S60 | 1 | "6549404".PN.  | USPAT;<br>USOCR   | OR | OFF | 2005/05/10 10:27 |
| S61 | 1 | "6452797".PN.  | USPAT;<br>USOCR   | OR | OFF | 2005/05/10 10:28 |
| S62 | 1 | "6157538".PN.  | USPAT;<br>USOCR   | OR | OFF | 2005/05/10 10:33 |
| S63 | 1 | "6023413".PN.  | USPAT;<br>USOCR   | OR | OFF | 2005/05/10 10:33 |

| S64 | 1   | "6157538".PN.                                 | USPAT;  | OR | OFF  | 2005/05/10 10:34 |
|-----|-----|---|---|----|------|------------------|
| 304 | 1   | 015/530 .PN.                                  | USOCR   | UK | OFF  | 2005/05/10 10:54 |
| S65 | 1   | "5793608".PN.                                 | USPAT;<br>USOCR   | OR | OFF  | 2005/05/10 10:35 |
| S66 | 1   | "6452797".PN.                                 | USPAT;<br>USOCR   | OR | OFF  | 2005/05/10 10:36 |
| S67 | 349 | vga and regulator                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/05/10 11:10 |
| S68 | 858 | (vga or agp) and regulator                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/05/10 11:11 |
| S69 | 12  | S68 and (inter adj integrated adj<br>circuit) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/05/10 11:11 |
| S70 | 350 | S68 and bus                                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/05/10 11:12 |
| S71 | 268 | S70 and display                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/05/10 11:12 |
| S72 | 1   | (vga adj card) and gpu and bus<br>and fan     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/05/23 15:50 |
| S73 | 2   | (vga adj card) and gpu and bus                | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON . | 2005/05/23 15:53 |

| S74 | 3    | (vga adj card) and gpu and fan  | US-PGPUB;   | OR   | ON | 2005/05/23 15:54 |
|-----|------|---|---|------|----|------------------|
|     |      |   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | ·    |    | ,                |
| S75 |      | vga and card and gpu and fan and<br>bus                                       | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/23 15:55 |
| S76 | 1    | vga and gpu and thermal and<br>management                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR . | ON | 2005/05/24 15:28 |
| S77 | 0    | vga and gpu and (component near detection)                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/24 14:39 |
| S78 | 1    | vga and gpu and (component with detection)                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/24 14:39 |
| S80 | 5    | vga and agp and cool  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/24 15:57 |
| S81 | 1342 | (monitoring adj device) and<br>detection and bus and display and<br>processor | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/25 15:35 |
| S82 | 11   | S81 and (inter adj integrated adj<br>circuit)                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2005/05/25 15:36 |

|          |     |  |   | _  |    |                  |
|----------|-----|--|---|----|----|------------------|
| S83      | 38  | S81 and (vga or gpu)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/25 15:47 |
| S84      | 155 | detection and (inter adj integrated<br>adj circuit) and bus and display<br>and processor | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/25 15:55 |
| S85      | 21  | vga and gpu  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/25 16:05 |
| S86      | 9   | (device near monitoring) and<br>(inter near integrated near circuit)<br>and regulator    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 09:35 |
| S87      | 652 | (device near monitoring) and regulator and bus   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 09:36 |
| S88      | 346 | S87 and display and detection  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:03 |
| S10<br>1 | 2   | "20030220721"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:06 |
| S10<br>2 | 2   | "6760649".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:49 |

|          |      |  |   | _  |    |                  |
|----------|------|--|---|----|----|------------------|
| S10<br>3 | 2    | "6134667".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:25 |
| S10<br>4 | 2    | "6006168".pn.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:25 |
| S10<br>5 | 0    | 700/301.ccls. and (inter near integrated near circuit)       | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:28 |
| S10<br>6 | 12   | "700"/\$.ccls. and (inter near integrated near circuit)      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:28 |
| S10<br>7 | 101  | "713"/\$.ccls. and (inter near integrated near circuit)      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:28 |
| S10<br>8 | 1    | "6760649".uref.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:41 |
| S10<br>9 | 2308 | detection and bus and processor<br>and display and regulator | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 11:49 |
| S11<br>0 | 35   | S109 and (inter adj integrated adj<br>circuit)               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 12:04 |

| S11<br>1 |     | ((vga or agp or gpu) near cool)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 12:07 |
|----------|-----|--|---|----|----|------------------|
| S11<br>2 | 3   | ((vga or agp or gpu) near thermal)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 12:07 |
| S11<br>3 | 22  | ((vga or agp or gpu) near<br>temperature)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 12:08 |
| S11<br>4 | 143 | (vga or agp or gpu) and regulate<br>and processor and display and<br>detection and bus | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 12:59 |
| S11<br>5 | 38  | (thermal adj management) and regulator and detection and bus and processor             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 14:05 |
| S11<br>6 | 65  | microbus and system  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 13:44 |
| S11<br>7 | 7   | (thermal adj management) and (graphic near cards)                                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/01 14:05 |